

Atty. Dkt. No. 069974-0143

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Keiichiro KATA et al.

Title:

PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE

AND SEMICONDUCTOR WAFER

Patent. No.:

RE39,603E

Issue Date:

05/01/2007

Examiner:

Mark V. Prenty

Art Unit:

2822

Confirmation 8077

Number:



## REQUEST FOR CERTIFICATE OF CORRECTION FOR **PURSUANT TO 37 C.F.R. § 1.323**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Enclosed, in duplicate, is a Certificate of Correction, Form PTO-SB/44, for United States Patent Number RE39,603E issued May 1, 2007.

Assignee:

**NEC Electronics Corporation** 

Kanagawa, Japan

Applicant submits that the noted errors do not constitute new matter, and correction thereof would not require reexamination.

Pursuant to 37 C.F.R. §1.323, Applicant requests that the enclosed Certificate of Correction be approved.

Since at least one of the noted errors is not the fault of the Patent Office, a credit card payment form is enclosed in payment of the required fee of \$100.00. The Commissioner is 10/29/2007 CNGUYEN2 00000146 RE39603

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100.00 OP

hereby authorized to charge any additional fees which may be required for this Request, to Deposit Account No. 19-0741.

Respectfully submitted,

Date: October 26, 2007

FOLEY & LARDNER LLP Customer Number: 22428 Telephone: (202) 945-6014 (202) 672-5399 Facsimile:

George C. Beck
Attorney for Applicant
Registration No. 38,072

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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

RE39.603E

APPLICATION NO.

10/645,782

**DATED** 

05/01/2007

INVENTOR(S)

Keiichiro Kata; Shinichi Chikaki

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Page 1, column 1, Assignee should read NEC Electronics Corporation, Kanagawa (JP) instead of NEC Corporation, Tokyo (JP)

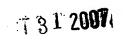
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